

Abstract of the Disclosure

After a DS3 signal is terminated by the DS3 interface circuit, an ATM cell is extracted by an ATM cell extraction circuit from a signal input through a DS3 network after performing an ATM mapping process. The ATM cell is mapped in a SONET frame by an ATM cell/N.A. new synchronous SONET STS mapping unit, and is transmitted to a SONET. When an STS signal is input from the SONET, an STS frame is terminated in an STS demapping circuit. An ATM cell is extracted by an ATM cell extraction circuit, and is mapped in the DS3 signal and transmitted by an ATM cell/N.A. asynchronous DS3 mapping unit.

09723117-112700